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**FAX COVER SHEET**

207.184-PH40US

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	USPTO		703-872-9306

**MESSAGE:** Attached please find an updated Issue Fee Transmittal correcting the Assignee information. Please contact us regarding any questions or comments. Thank you.

**FROM:** KELLY K. KORDZIK/SB                    **PHONE NO.:** 512.370.2851  
**DATE:** FEBRUARY 10, 2004                    **TIME IN:**

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Severna Beller	(Depositor's name)
<i>Severna Beller</i>	(Signature)
2/10/04	(Date)

APPLICATION NO	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO	CONFIRMATION NO.
09/533,619	03/22/2000	Angela T. Hsu	1376P/D922	7885

**TITLE OF INVENTION: METHOD AND SYSTEM FOR REDUCING CHARGE GAIN AND CHARGE LOSS WHEN USING AN ARC LAYER IN INTERLAYER DIELECTRIC FORMATION**

APPLN TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	03/01/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
LOUIE, WAI SING	2814	438-622000

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## (A) NAME OF ASSIGNEE

Advanced Micro Devices, Inc.  
Fujitsu Limited  
Fujitsu and Semiconductor Ltd.

Sunnyvale, CA 94086  
Kanagawa 211-88, Japan  
Fukushima Pref. 965-845, Japan

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<i>Kelly K. Kordzik, Reg. No. 36,571</i>	<i>2-2-04</i>

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